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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Application of Janet L. Pan

For: USE OF DEEP-LEVEL TRANSITIONS IN

SEMICONDUCTOR DEVICES

S/N: 10/654,790

Art Unit: 2818

Examiner: M. Tran

AMENDMENT

Applicant submits this amendment in response to the Examiner's Official Action, dated 4/13/05, in the above-captioned application.

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